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- 1 [A frame based system for representing knowledge about VLSI design: a proposal](#)
W. Stephen Adolph, Hassan K. Reghbaty, Amar Sanmugasunderam
July 1986 **Proceedings of the 23rd ACM/IEEE conference on Design automation DAC '86**

Publisher: IEEE Press

Full text available: pdf(654.40 KB)

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Engineers use large amounts of stereotype knowledge about basic circuit components and design techniques when designing a circuit. This knowledge is used to guide design decisions in order to produce an optimal design. Such stereotyped knowledge can be represented as frames in a knowledge based system and such a knowledge base can be used to implement a silicon compiler. This paper describes a pilot system we are developing at SFU which uses frames to represent abstract models of circuit co ...

Keywords: VLSI design, computer assisted design, knowledge representation, silicon compilation

- 2 [Flows and bottleneck monitoring: Bottleneck detection in UMTS via TCP passive monitoring: a real case](#)



Fabio Ricciato, Francesco Vacirca, Martin Karner

October 2005 **Proceedings of the 2005 ACM conference on Emerging network experiment and technology CoNEXT'05**

Publisher: ACM Press

Full text available: pdf(469.35 KB)

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In this work we address the problem of inferring the presence of a bottleneck from passive measurement in the UMTS core network. The study is based on one month of packet traces collected in the core network of mobilkom austria AG & Co KG, the leading mobile telecommunications provider in Austria, EU. During the measurement period a bottleneck link in the UMTS core network was revealed and removed, therefore the traces enable the accurate analysis and comparison of the traffic behavior in the tw ...

Keywords: UMTS, bottleneck detection

- 3 [Performance profiling and tuning: DEP: detailed execution profile](#)